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10/678,769

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Stephen P. Robb et al.
Serial No.: 10/678,769
Filing Date: November 19, 2003
Group Art Unit: 2814
Examiner: Ngan V. Ngo
Title: **POWER SWITCH STRUCTURE WITH LOW RDSON
AND LOW CURRENT LIMIT AND METHOD**

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED
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Date of Deposit

Semiconductor Components Industries, LLC.

Name of Assignee

Lydell J. Chapman

12/9/04

SIGNATURE

DATE

**RESPONSE TO RESTRICTION REQUIREMENT
AND PRELIMINARY AMENDMENT**

HONORABLE COMMISSIONER FOR PATENTS,
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

In response to the Office Action mailed November 19, 2004,
please reconsider the above-identified patent application in view
of the amendments and remarks presented hereinafter. Applicant
hereby elects the Group 1 claims (claims 1-16) without traverse.